

EAST - [default.wsp:1]

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UDC

Queue

L1: (510462) \$symmetr\$5

L9: (926) (polyphase or poly adj phase) adj1 filter\$3

L10: (307) 1 and 9

L11: (140) ((708/319).CCLS.) and \$symmetr\$5

L14: (299) 10 not 11

L15: (956) direct same transpos\$3

L16: (2) 14 and 15

L17: (3607994) reconfigur\$5 programmable select\$4

L18: (252) 14 and 17

L19: (183) delay\$3 and 18

L20: (32115) adder\$1 same multiplier\$3

L21: (95) 19 and 20

L22: (88) 19 not 21

L23: (69) 18 not 19

L24: (47) 14 not 18

(2) ("20020184275").PN.

(2) ("5892695").PN.

(396) (708/319).CCLS.

(4) ("4263593" | "4344149" | "5081604" | "5206821").PN.

(4) ("4489393" | "4972356" | "5126961" | "5148384").PN.

(16) 5339264.URPN.

USPAT US:PGPUB.EPO JPO DERWENT EML TOB

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	U	K	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval CI	Inventor	S	C	P	3	U
1			US 5339264 A	19940816	17	Symmetric transposed FIR digital filter	708/319	708/313		Said, Ahmed et al.					U
2			US 5206821 A	19930427	24	Decimation circuit employing multiple memory/data shifting	708/313			Young, William R. et al.					U
3			US 6260053 B1	20010710	23	Efficient and scalable FIR filter architecture for decimation	708/313	708/319		Maulik, Prabir C. et al.					U

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